Amendments to the Specification

Please replace the text from page 14, lines 4-16 with the following:

Referring now to **Fig. 2d**, an example register **122** suitable for use to facilitate configuration of eligible wake events for waking system **100**, in accordance with one embodiment, is shown. As illustrated, register **122** includes a number of storage locations for storing a number of data bits to indicate (in accordance with the bit values) whether corresponding wake events are eligible or ineligible to wake system **100**. For the embodiment, register **122** includes in particular bits **272**-**276** to indicate wake eligibility of wake events caused by a real time clock (RTC), universal serial bus (USB) activities, and modem activities respectively. Register **122** further includes bits **278-280** to indicate wake eligibility of wake events initiated by one of a number of peripheral control interface (PCI) devices (PME wake), and AC re-availability, respectively. In alternate embodiments, more or less configurable wake events may be supported.